

Notice of References Cited	Application/Control No. 10/734,020	Applicant(s)/Patent Under Reexamination LEE ET AL.	
	Examiner Belur V Keshavan	Art Unit 2825	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,147,507	11-2000	Shabde et al.	324/766
	B	US-5,585,734	12-1996	Meuris et al.	324/719
	C	US-6,147,507	11-2000	Shabde et al.	324/766
	D	US-2003/0057988	03-2003	Maeda et al.	324/765
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	B. El-Kareh, Introduction to VLSI Silicon Devices, pp.93
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.